

SECTION III – A MARK-UP VERSION OF THE AMENDED PENDING
CLAIMS

Please amend the claims as follows:

34. (Twice Amended) A storage device, comprising:

a memory buffer;

a storage medium; and

a controller circuit coupled to the memory buffer and the storage medium, the storage device and a separate storage device to couple to a host computer through a same physical port having an Intelligent Drive Electronics (IDE) interface, the controller circuit to receive data through the same physical port having the IDE interface and to store the data into the buffer, the controller circuit to transmit the data from the buffer to the storage medium simultaneously at least in part with the separate storage device transmitting and/or receiving data using the same physical port having the IDE interface.

37. (Once Amended) The storage device of claim 34, wherein the controller circuit includes a register, wherein bits stored in the register have a first state for indicating that the storage device is receiving data through the same physical port having the IDE interface and have a second state for indicating that the storage device is not receiving data through the same physical port having the IDE interface.

1 40. (Twice Amended) A storage device, comprising:

2 a memory buffer;

3 a storage medium; and

4 a controller circuit coupled to the memory buffer and the storage medium, the

5 storage device and a separate storage device to couple to a host computer through a same



6 physical port having an Intelligent Drive Electronics (IDE) interface, the controller circuit
7 to read data from the storage medium and to store the data in the buffer simultaneously at
8 least in part with the separate storage device transmitting and/or receiving data on the
9 same physical port having the IDE interface, the controller circuit to transmit the data
10 from the buffer through the same physical port having the IDE interface.

1 43. (Once Amended) The storage device of claim 40, wherein the controller circuit
2 includes a register, wherein bits stored in the register have a first state for indicating that
3 the storage device is receiving data through the same physical port having the IDE
4 interface and have a second state for indicating that the storage device is not receiving
5 data through the same physical port having the IDE interface.

1 46. (Once Amended) A storage device comprising:

2 a buffer to couple to a host computer through a physical port having an Intelligent
3 Drive Electronics (IDE) interface, the physical port having the IDE interface also being
4 coupled to a separate storage device, wherein the host computer can communicate data
5 through the physical port having the IDE interface with only one of the storage device or
6 the separate storage device at any given time;

7 a storage medium coupled to the buffer;

8 a first circuit to transfer data between the storage medium and the buffer
9 responsive to commands from the host computer;

10 a second circuit to transfer data between the buffer and the host computer over the
11 physical port having the IDE interface; and

12 a third circuit to release the physical port having the IDE interface for use with the
13 separate storage device while the data is being transferred between the storage medium
14 and the buffer.

1 51. (Once Amended) A computer system comprising:

2 a host computer;

3 a physical port having an interface coupled to the host computer, wherein only
4 one device can communicate with the host computer over the physical port having the
5 interface at any given time

6 a first storage device coupled to the physical port having the interface;

7 a second storage device coupled to the physical port having the interface using the
8 same pin out and pin description as the first storage device and including:

9 a storage medium;

10 a buffer coupled between the physical port having the interface and the
11 storage medium; and

12 control circuitry to release the physical port having the interface for use
13 with the first storage device while data is being transferred between the storage medium
14 and the buffer.

1 57. (Once Amended) A method comprising:

2 transmitting from a host computer over a single physical port having an Intelligent
3 Drive Electronics (IDE) interface, to which a first storage device and a second storage
4 device are coupled, a first command to the second storage device, wherein data can be
5 transmitted between the host computer and only one of the first storage device and the
6 second storage device at any given time; and

7 releasing the physical port having the IDE interface for use with the first storage
8 device while the second storage device is accessing a tape medium in the second storage
9 device responsive to the first command.

1 59. (Once Amended) The method of claim 57, further including:



2 transmitting from the host computer over the single physical port having the IDE
3 interface a second command to the first storage device, wherein the second command is a
4 read or write command; and

5 releasing the single physical port having the IDE interface for use with the second
6 storage device only after the first storage device has completed execution of the second
7 command.

1 60. (Once Amended) The method of claim 59, further including:

2 the single physical port having the IDE interface receiving an interrupt signal over
3 an interrupt line shared by the first storage device and the second storage device; and

4 the host computer responding to the interrupt signal based on which of the first
5 storage device and the second storage device currently control the single physical port
6 having the IDE interface.

1 61. (Once Amended) The method of claim 57, further including:

2 transmitting from the host computer over the single physical port having the IDE
3 interface a second command to the first storage device, wherein the second command is a
4 read or write command; and

5 releasing the single physical port having the IDE interface for use with the second
6 storage device only after the first storage device has completed accessing a disk storage
7 medium in the first storage device responsive to the second command.

1 62. (Once Amended) The method of claim 57, further including:

2 the single physical port having the IDE interface receiving an interrupt signal over
3 an interrupt line shared by the first storage device and the second storage device; and



the host computer responding to the interrupt signal based on which of the first storage device and the second storage device currently control the single physical port having the IDE interface.

63. (Once Amended) The method of claim 57, wherein:

the first command is a read command; and
the method further includes transmitting the data from the buffer in the second storage device to the host computer over the single physical port having the IDE interface after releasing the single physical port having the IDE interface.

66. (Twice Amended) A method comprising:

transmitting, from a host computer over a single physical port having an Intelligent Drive Electronics (IDE) interface to which a first storage device and a second storage device are coupled, a command wherein data can be transmitted between the host computer and only one of the first storage device and the second storage device at any given time;

transmitting data between the host computer and a buffer in the second storage device over the single physical port having the IDE interface responsive to the command; transmitting data between the buffer and a storage medium in the second storage device responsive to the command; and

transmitting data between the host computer and the first storage device over the single physical port having the IDE interface simultaneous with at least part of the transmitting of data between the buffer and the storage medium.

69. (Twice Amended) The method of claim 66, further including:



transmitting, from the host computer over the single physical port having the IDE interface, a second command to the first storage device, wherein the second command is a read or write command; and

releasing the single physical port having the IDE interface for use with the first storage device only after the second storage device has completed execution of the second command.

70. (Once Amended) The method of claim 69, further including:

the single physical port having the IDE interface receiving an interrupt signal over an interrupt line shared by the first storage device and the second storage device; and

the host computer responding to the interrupt signal based on which of the first storage device and the second storage device currently control the physical port having the IDE interface.

71. (Once Amended) The method of claim 66, further including:

the single physical port having the IDE interface receiving an interrupt signal over an interrupt line shared by the first storage device and the second storage device; and

the host computer responding to the interrupt signal based on which of the first storage device and the second storage device currently control the physical port having the IDE interface.

72. (Once Amended) The method of claim 66, wherein:

the transmitting, from the host computer over the single physical port having the IDE interface, of the command to the second storage device includes:

setting an indicator to indicate that the single physical port having the IDE interface is busy;



6 transmitting, from the host computer over the single physical port having
7 the IDE interface to the second storage device, a write command; and
8 the transmitting of data between the host computer and the first storage device
9 includes:
10 setting the indicator to indicate that the single physical port having the
11 IDE interface is no longer busy,
12 transmitting data between the host computer and the first storage device
13 over the single physical port having the IDE interface, and
14 writing at least some of the data from the buffer to the storage medium in
15 the second storage device subsequent to the setting of the indicator to indicate that the
16 single physical port having the IDE interface is no longer busy and concurrently with the
17 transmitting of data between the host computer and the first storage device over the single
18 physical port having the IDE interface.

